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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/710.616	07/25/2004	Jung-Ching Chen	NAUP0575USA	4015

TITLE OF INVENTION: METHOD FOR FABRICATING INTEGRATED CIRCUITS HAVING BOTH HIGH VOLTAGE AND LOW VOLTAGE DEVICES

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P.O. BOX 506, Merrifield, VA 22116, U.S.A.

Voice Mail: 302-729-1562

FAX: 806-498-6673

e-mail:winstonhsu@naipo.com

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Winston Hsu 02/01/2007

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